

# ISDN S- INTERFACE MODULES

## SMALLEST SMT PACKAGE-EN60950/IEC950 DESIGNED

- The transmission pulse mask and impedance according to ITU-T-I.430
- Low or high frequency common mode choke option available
- Withstands temperature  $220\pm 5^{\circ}\text{C}$  IR Reflow
- Operating temperature range  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$
- Storage temperature range  $-20^{\circ}\text{C}$  to  $+105^{\circ}\text{C}$
- 4mA Idc Max. Permissible Unbalance DC Current
- Small package for saving space: L/W/H(inch)0.92"x0.55"x0.354"(23.2x14x9mm)
- 3.0KVrms, in according to EN60950/IEC950

### MODULES SELECTION GUIDE

Chip Manufacturer	Chip Designation	Dual Transformer	Module With 50uH CMC	Module With 100uH CMC	Module With 500uH CMC
<b>AMD</b>	AM79C30A/32A	UT20765-00TS	UT20765-05TS	UT20765-10TS	UT20765-50TS
<b>Cologne Chip</b>	HFC-S	UT20765-00TS	UT20765-05TS	UT20765-10TS	UT20765-50TS
<b>Lucent</b>	T7234/T7250/ T7256/T7259	UT20768-00TS	UT20768-05TS	UT20768-10TS	UT20765-50TS
	T7903	UT20765-00TS	UT20765-05TS	UT20765-10TS	UT20765-50TS
<b>Mietec</b>	MTC-20172	UT20765-00TS	UT20765-05TS	UT20765-10TS	UT20765-50TS
<b>Mitel</b>	MT8930/8931	UT20765-00TS	UT20765-05TS	UT20765-10TS	UT20765-50TS
<b>Motorola</b>	MC145474/ 145475	UT20763-00TS	UT20763-05TS	UT20763-10TS	UT20763-50TS
	MC145574	UT20768-00TS	UT20768-05TS	UT20768-10TS	UT20768-50TS
	MC145575	UT20765-00TS	UT20765-05TS	UT20765-10TS	UT20765-50TS
<b>National</b>	TP3420/3421	UT20765-00TS	UT20765-05TS	UT20765-10TS	UT20765-50TS
<b>SGS Thomson</b>	ST5420/5421	UT20765-00TS	UT20765-05TS	UT20765-10TS	UT20765-50TS
<b>Siemens</b>	PEB2080/2081/ 2084/2085 PEB2086/ PSB2186	UT20765-00TS	UT20765-05TS	UT20765-10TS	UT20765-50TS
	PSB21381-84SX "SCOUT"	UT20763-00TS	UT20763-05TS	UT20763-10TS	UT20763-50TS
	PEB20590	UT20763-00TS	UT20763-05TS	UT20763-10TS	UT20763-50TS
<b>Yamaha</b>	YM7405B	UT20765-00TS	UT20765-05TS	UT20765-10TS	UT20765-50TS

1. CMC : COMMON MODE CHOKE
2. Various of choke inductance are available.

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## SMALLEST SMT PACKAGE-EN60950/IEC950 DESIGNED

ELECTRICAL SPECIFICATION @25°C:

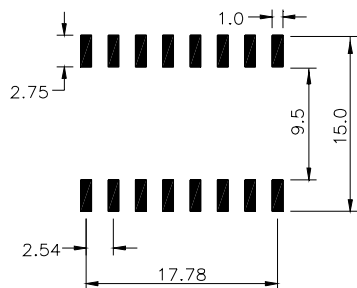
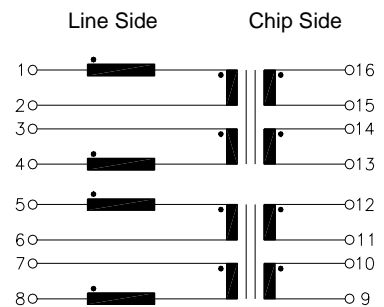
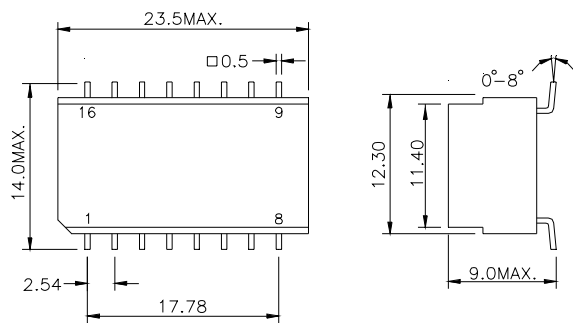
PART NO.	Turns Ratio (Line:Chip) (±2%)	Line Side Inductance (mH min.)	Interwinding Capacitance (pf max)	Line Side Leakage Inductance (uH max)	DCR Line Side (Ω ±15%)	DCR Chip Side (Ω ±15%)
UT20763-**TS	1split:1split	30	20	12	3.3	1.9
UT20765-**TS	1split:2split	30	20	12	3.3	3.8
UT20768-**TS	1split:2.5split	30	20	12	3.3	4.8

PART NO.	Common mode choke							
	-00TS		-05TS		-10TS		-50TS	
	OCL (uH)	DCR (Ω max.)	OCL (uH)	DCR (Ω max.)	OCL (uH)	DCR (Ω max.)	OCL (uH)	DCR (Ω max.)
UT20763-**TS	None		50	0.4	100	0.6	500	0.9
UT20765-**TS	None		50	0.4	100	0.6	500	0.9
UT20768-**TS	None		50	0.4	100	0.6	500	0.9

Test Condition:

1. Inductance : Main Transformers 10KHz 100mVrms. CMC 100KHz 20mVrms.
2. Leakage Inductance & Interwinding Capacitance : 100KHz 100mVrms.

### PACKAGES & SCHEMATICS



Unit : mm  
Tolerance:±0.25mm

\*Specifications are subject to change without prior notice.

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