



• **ISDN s₂M-Interface Transformer*:**

• **Electrical specifications at 25°C:**

UMEC Part No.	Ratio:n ±2%	L _H mH Min.	L _S µH Max.	R _{CU,IC} ΩNom.	R _{CU,L} ΩNom.	U _P KVrms	figure/ schematic
DIP Flat-Design:*							
UT21137	1.5:1.5:1:1.5:1.5	5	1	0.41	0.3	2.0	B
UT21141	1:1:2	2	1	0.3	0.3	2.0	C
UT21190	1:1.41	4	1	0.3	0.5	2.0	D
UT21196	2/8/8/2:5/4/1	2	4	0.6	0.2	2.0	A
DIP Upright-Design:*							
UT21527	1.5:1.5:1:1.5:1.5	5	1	0.41	0.3	2.0	B
UT21556	1:1:2	2	1	0.3	0.3	2.0	C
UT21519	1:1.41	4	1	0.3	0.5	2.0	D
UT21581	2/8/8/2:5/4/1	2	4	0.6	0.2	2.0	A
Surface Mount Design:*							
UT21137-TS	1.5:1.5:1:1.5:1.5	5	1	0.41	0.3	2.0	B
UT21141-TS	1:1:2	2	1	0.3	0.3	2.0	C
UT21190-TS	1:1.41	4	1	0.3	0.5	2.0	D
UT21196-TS	2/8/8/2:5/4/1	2	4	0.6	0.2	2.0	A
UT 28432-TS	1:1.41	4	1	0.65	0.5	2	A
EN60950-Design:*							
UT 21137N*	1.5:1.5:1:1.5:1.5	5	1	0.41	0.3	3.0	B
UT 21190-N*	1:1.41	4	1	0.3	0.5	4.0	D
UT 20788	1.41CT:1CT	1.2	0.8	0.6	0.8	3.0	A
UT 20488 Dual	1.41CT:1CT/ 1.41CT:1CT	1.2	0.8	0.6	0.8	3.0	A
UT21762	1:1:1.2	2	5	0.8	0.35	4.0	A
UT21763	1:1:1	2	5	1.0	0.4	4.0	A
UT21764	2.5:2.5:1	2	5	2.4	0.4	4.0	A
UT21765	2:2:1	2.5	5	2.0	0.4	4.0	A
UT21766	1:1:2	2	5	0.6	0.4	4.0	A
UT21704	1:1:1.57	2	1	0.46	0.11	4.0	A
UT21706	1:1:1	12	5	1.40	0.25	4.0	A
UT21712	1:1:2	2	1	0.40	0.10	4.0	A
UT21713	1:1:2.67	3.5	1	0.40	0.13	4.0	A

*)Ferrite design solution **UT 28xxx**.

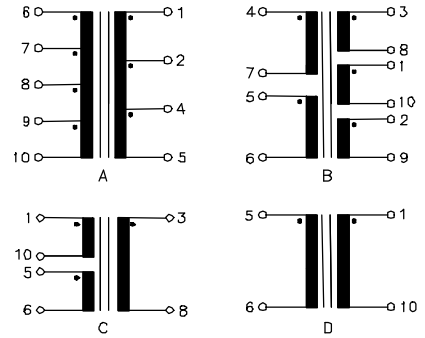
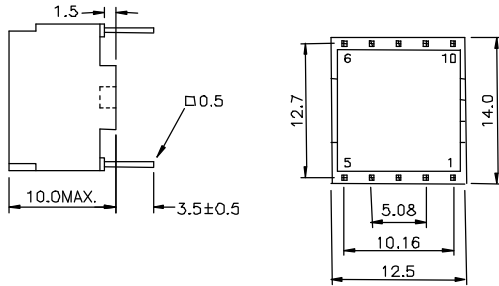




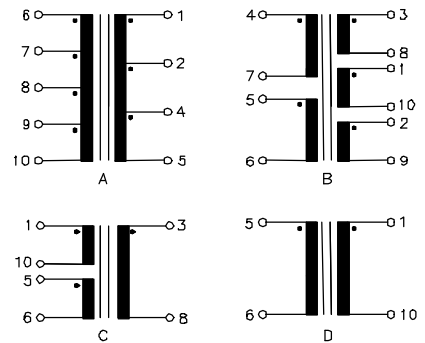
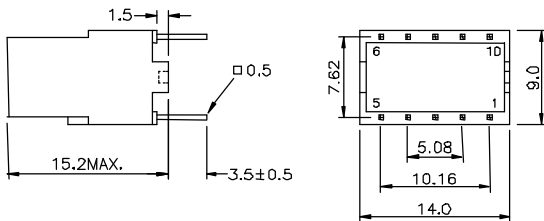
• **ISDN s₂M-Interface Transformer***:

Dimensions and connections(tolerance=±0.2mm)

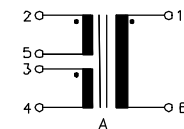
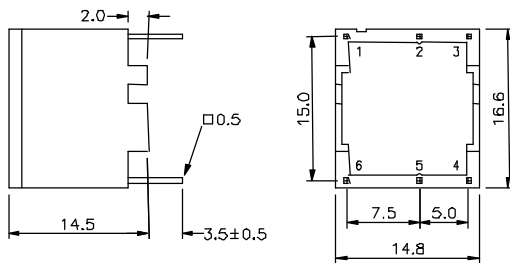
UT211..



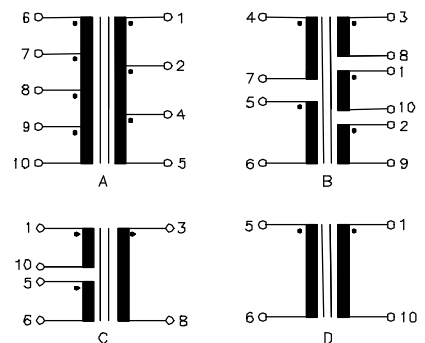
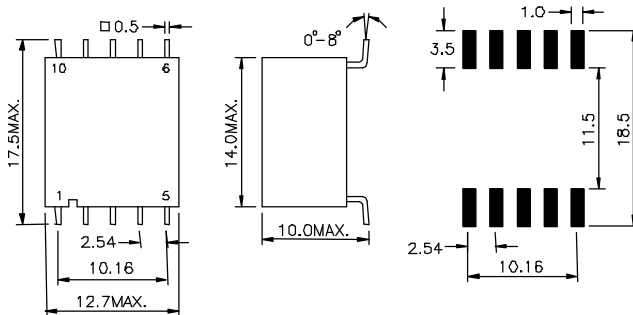
UT215..



UT217..



UT211..-TS(SMT design)*)



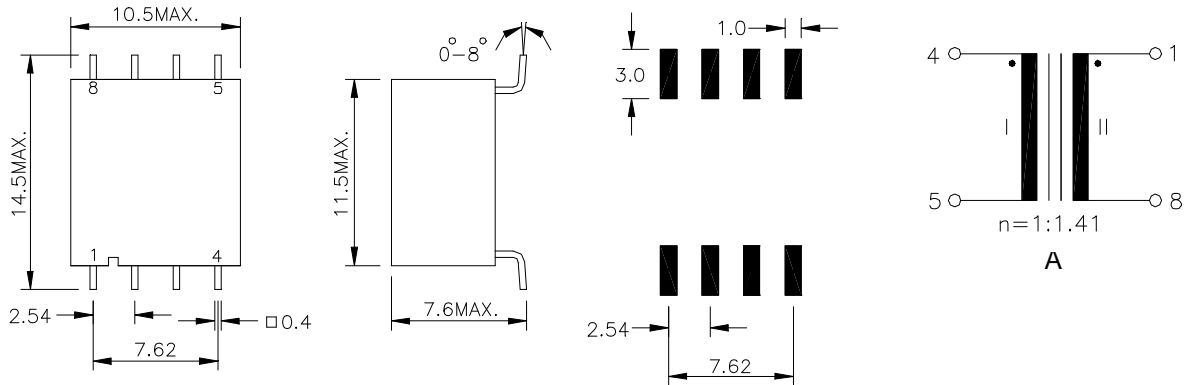
*) pins arrangement according to customer requirement.



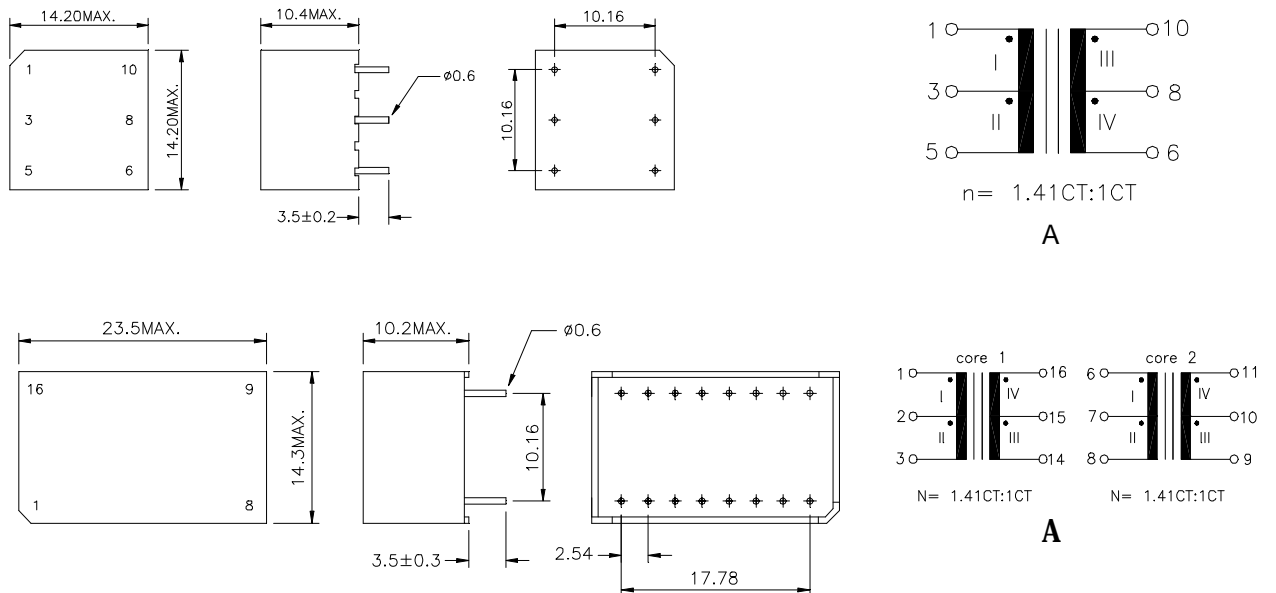


• ISDN s₂M-Interface Transformer*:

UT 284xx-TS (SMT-Design):



U I 20488/-788:



De

n= transformer ratio: IC-side:Line-side.

L_H= main inductance of winding(s) on Line-side(in series, f=10KHz U=100mVrms).

L_S= leakage inductance of winding(s) on Line-side with winding(s) on IC-side short circuited(each in series, f=100KHz U=100mVrms).

R_{CU.IC}= DC resistance of the winding(s) on IC-side(in series, nominal value).

R_{CU.L}= DC resistance of the winding(s) on Line-side(in series, nominal value).

U_P= test voltage, rms value 50/60Hz, 2seconds, winding(s) on Line-side to winding(s) on IC-side.

