



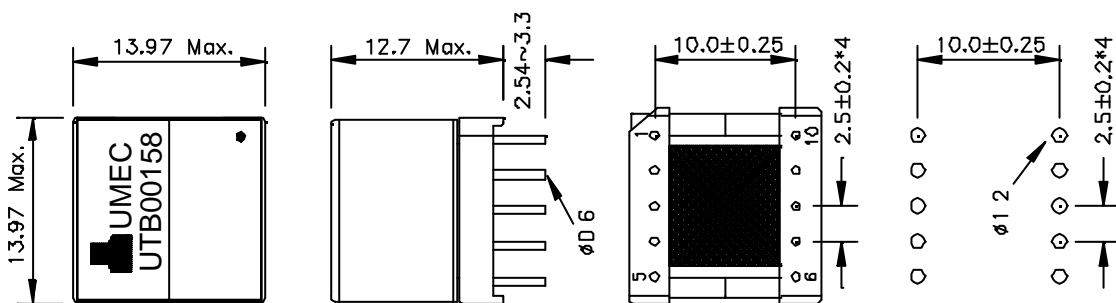
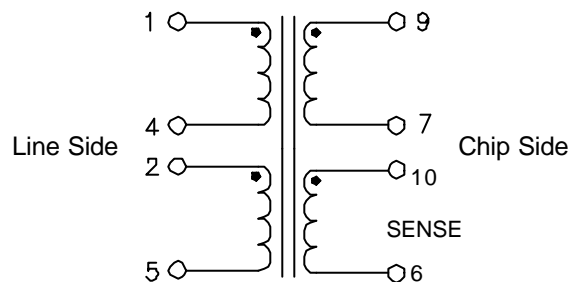
# SHDSL

UMEC P/N:	DESCRIPTION	REVISION	DATE
UTB00158	Infineon PEB24622 CO/PEB22622 CPE SHDSL,EP13,TH,Line Transformer Supplementary Insulation,TNV3 to SELV,250V Working Voltage	A6	02/04

### ELECTRICAL SPECIFICATIONS AT 25°C:

- INDUCTANCE: 3.0mH±10%, 10kHz, 100mV, 1-5 (tie 2&4)  
3.0mH±10%, 10kHz, 100mV, 80mA DC BIAS, 1-5 (tie 2&4)
- LEAKAGE INDUCTANCE: 18-25µH max, 100kHz, 100mV, 1-5 (tie 2&4, 9&7)
- URNS RATIO: 3.20 : 1.00, ( 1-5 ):( 9-7 ) ( tie 2&4 )  
(@ 10kHz, 100mVAC, ±2%) 1.00:1.00,(1-4):(2-5)  
1.00:1.00,(10-6):(9-7)
- DC RESISTANCE @25°C: 2.75 max., 1-5 (tie 2&4)  
0.80 max., 9-7  
3.10 max., 10-6
- HI-POT: 2000Vac, 1s, 500µA max., 1-10 ( tie 2&4,9&6 )
- SAFETY: MEETS REQUIRMENTS OF UL1950, EN60950 AND IEC950  
SUPPLEMENTARY INSULATION, 250V WORKING VOLTAGE
- OPERATION TEMPERATURE: -40°C ~ +85°C

### Dimensions:



SUGGESTED P.C.B. LAYOUT

UNIT: mm

### AGENCY APPROVALS

UL1950	: FILE NO. E212730	IEC950/EN60950	: B.E.A.B. ref # ITE6357
CSA 950(via C-UL)	: FILE NO. E212730	Nordic, Japan, & Australia Deviations	: B.E.A.B. ref # ITE6357

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